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Art Unit 2812

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Re:

U.S. Utility Patent Application

Application No. 10/697,889; Filed: October 31, 2003

For: Method for Manufacturing a Memory Cell for Modification of

Revision Identifier in an Integrated Circuit Chip

Inventors:

CATALASAN et al.

Our Ref:

1875.4360003

Sir:

Transmitted herewith for appropriate action are the following documents:

- 1. Status Inquiry; and
- 2. Return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

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Commissioner for Patents March 15, 2006 Page 2

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Donald J. Featherstone Attorney for Applicants Registration No. 33,876

DJF/amr Enclosures

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

CATALASAN et al.

Appl. No.: 10/697,889

Filed: October 31, 2003

For: Method for Manufacturing a

Memory Cell for Modification of Revision Identifier in an Integrated

Circuit Chip

Confirmation No.: 9019

Art Unit: 2812

Examiner: To Be Assigned

Atty. Docket: 1875.4360003

Status Inquiry Letter

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

Please advise the undersigned of the present status of the above-identified patent application.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Attorney for Applicants Registration No. 33,876

Date: <u>3/70/0</u>

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